

PCB Hardware Design Engineer

Overview

Position: Senior RF Hardware Design Engineer

Reporting to: Director of Package and Board Design

Location: Redondo Beach, CA

Job Summary

An exciting new fabless semiconductor company seeks an experienced RF Hardware Engineer to develop printed circuit boards for highly integrated monolithic and multi-chip solutions. The position will consist of developing RF The candidate will be involved with the development of new-to-the-world ultra-high speed RF CMOS SOC's and MCM's for a wide range of high frequency commercial, military, and aerospace applications. The person will work with industry leading IC design, package design, and systems engineering experts that are involved in the development of state-of-the-art hybrid digital/RF receivers and transmitters.

Essential Duties

- Circuit board planning:
 - Decide upon interface connectors and major active components, if any
 - Summarize RF signal and DC current requirements
 - Predict the stackup and determine the RF stripline transmission line layers
 - Forward the package mechanical information and BGA ballmap
- Circuit board design:
 - Design boards with analog bandwidths exceeding 40 GHz and digital data rates up to 30 Gbps
 - Create Allegro schematics and symbols
 - Manage circuit board layout and make critical routing changes using Allegro
 - Work with team members to insure board performance and suitability
 - Troubleshoot hardware performance concerns in the lab

Required Skills and Experience

At least 5 years' experience with RF hardware engineering is a plus

- Thorough understanding of board layout techniques
- Intuitive understanding of transmission line, and power supply decoupling routing methodologies
- Experience with Cadence Orcad and Allegro software
- Flexibility in handling shifting priorities and variable workloads
- BSEE degree or higher
- **US citizenship required**